SEMICONDUCTOR DEVICE JP5166992 Patent Number: 1993-07-02 Publication date: TSUJI KAZUTO; others: 02 Inventor(s): FUJITSU LTD Applicant(s): Requested Patent: ☐ JP5166992 Application Number: JP19910330742 19911213 Priority Number(s): IPC Classification: H01L23/50; B65D85/00; B65D85/38 EC Classification: Equivalents: Abstract PURPOSE:To accurately position a semiconductor device which is delivered as it is mounted on a protective frame by the use of protective frame. CONSTITUTION: A semiconductor main body 16, a frame 18 provided outside outer leads 14b so as to surround the semiconductor main body 16, and a support 19 provided between the semiconductor main body 16 and the frame 18 to support the semiconductor main body 16 on the frame 18 are provided, where

the frame 18 and the support 19 constitute a protective frame 17 of integral structure.

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